

## **LN1F60**

# General Rectifying Diodes 600V, 1.1A

#### **Feature**

- Small SMD
- Low Noise
- Low VF
- Pb free terminal
- RoHS:Yes

## **OUTLINE**

Package (House Name): 1F
Package (JEDEC Code): DO-214AC

## **Equivalent circuit**



## **Absolute Maximum Ratings** (unless otherwise specified : Tl=25°C)

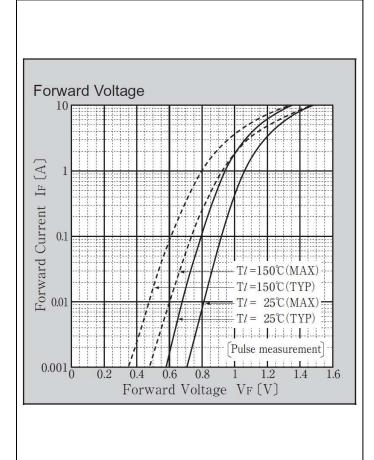
Item	Symbol	Conditions	Ratings	Unit
Storage temperrature	Tstg		-55 to 150	°C
Junction temperature	Tj		-55 to 150	°C
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, On alumina substrate, Ta=25°C %	1.1	А
Average forward current	I <sub>F</sub> (AV)	50Hz sine wave, Resistance load, On glass-epoxy substrate, Ta=25°C %	0.8	Α
Surge forward current	I <sub>FSM</sub>	50Hz sine wave, Non-repetitive 1 cycle peak value, Tj=25°C	25	Α
Surge forward current	I <sub>FSM1</sub>	tp=1ms, sine wave, Non-repetitive, peak value, Tj=25°C	80	А

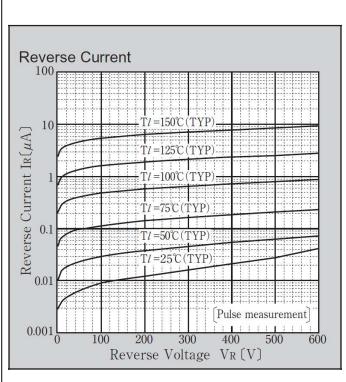
**<sup>※</sup>** ∶See the original Specifications

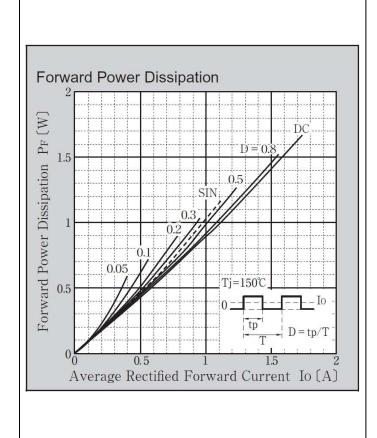
## **Electrical Characteristics** (unless otherwise specified: Tl=25°C)

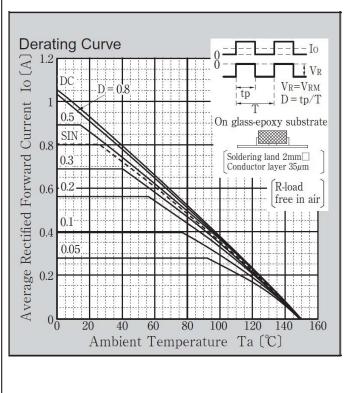
Item	Symbol	Conditions	Ratings			l lmia
			MIN	TYP	MAX	Unit
Forward voltage	$V_{F}$	IF=0.8A, Pulse measurement			1.05	V
Reverse current	I <sub>R</sub>	VR=600V, Pulse measurement			10	μΑ
Reverse recovery time	trr	IF=0.1A, IR=0.1A, 0.1IR			3.5	μs
Thermal resistance	Rth(j-l)	Junction to lead			23	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On alumina substrate *			108	°C/W
Thermal resistance	Rth(j-a)	Junction to ambient, On glass-epoxy substrate *			157	°C/W

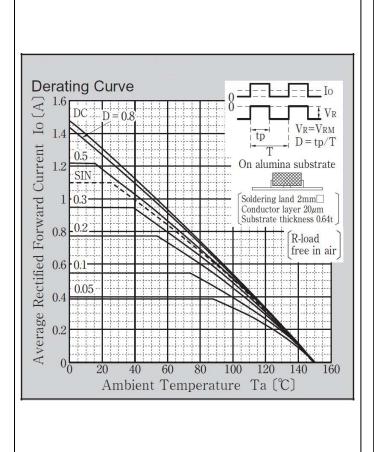
## **CHARACTERISTIC DIAGRAMS**

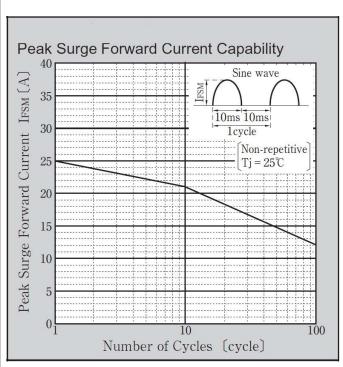


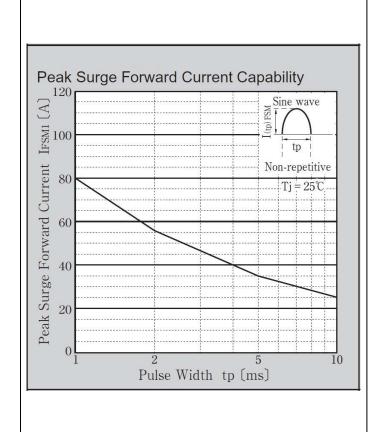


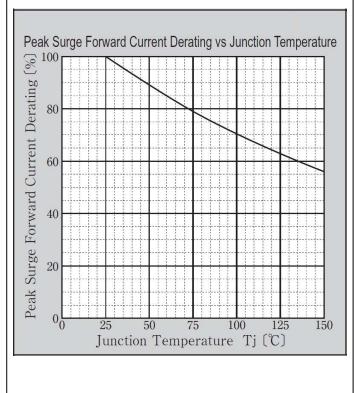


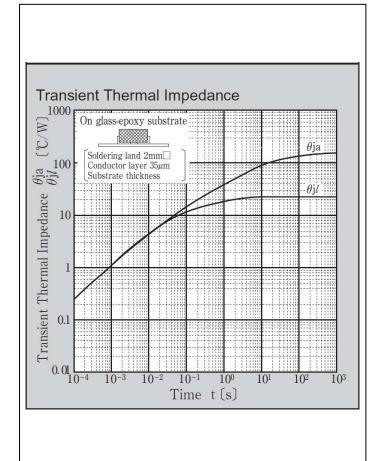


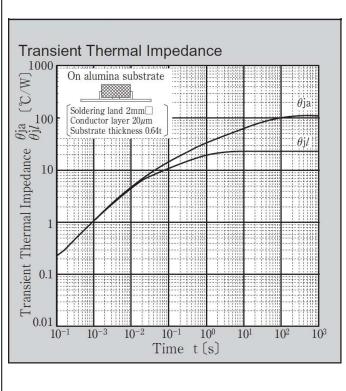








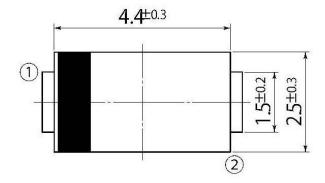


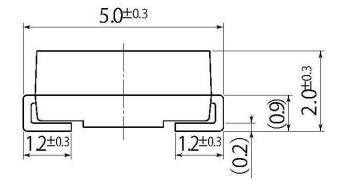


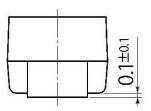
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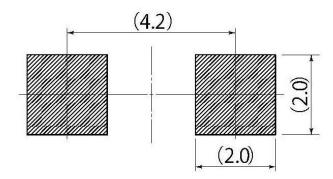
**B**3

JEDEC Code	DO-214AC		
JEITA Code	_		
House Name	1F, CF		









Referential Soldering Pad

• Optimize soldering pad to the board design and soldering condition.

#### **Notes**

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#### [Specific applications]

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